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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/064,916	08/29/2002	Ching-Fang Yen	7558-US-PA	9010
31561	7590	12/27/2005	EXAMINER	
JIANQ CHYUN INTELLECTUAL PROPERTY OFFICE 7 FLOOR-1, NO. 100 ROOSEVELT ROAD, SECTION 2 TAIPEI, 100 TAIWAN			BRAGDON, REGINALD GLENWOOD	
			ART UNIT	PAPER NUMBER
			2185	
DATE MAILED: 12/27/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/064,916

Applicant(s)

YEN ET AL.

Examiner

Reginald G. Bragdon

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 14 November 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 and 10-17 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,6-8,10 and 15-17 is/are rejected.
- 7) ☒ Claim(s) 2-5 and 11-14 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Objections

1. Claims 1-8 and 14 are objected to because of the following informalities:

As per claim 1, line 13, "having the total memory capacity" appears redundant and should be removed.

As per claim 4, line 2, "type non-volatile second" should be --second type non-volatile--.

As per claim 14, line 3, --at least one-- should be added before "replacement".

As per claim 14, line 4, --at least one-- should be added before "replacement".

All dependent claims are objected to as having the same deficiencies as the claims they depend from.

Appropriate correction is required.

Claim Rejections - 35 USC § 112

2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

3. Claims 6-8 and 15-17 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

As per claim 6, lines 12-13, "other than segments replaced by the first type non-volatile memory device" is not clear in context. It is noted that "segments replaced by the first type non-volatile memory device" have not been set forth previously in claims 1 and/or 6, nor has any

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relationship between the “replacement area” and the “first type non-volatile memory device” been set forth.

As per claim 7, it is not clear what Applicant is trying to set forth in this claim. In lines 1 and 4, it is not clear what is meant by “the in the replacement memory area”. In line 4, “the replacement memory area that is currently being replaced by the first type non-volatile memory device” lacks antecedent basis since a first type non-volatile memory device has not been set forth specifically as replacing the replacement memory area.

As per claim 8, lines 3-4, “the currently-accessed memory device” lacks antecedent basis. In line 4, “the one second memory replacement segment” lacks antecedent basis since no particular second memory replacement segment has been set forth previously. In lines 5-6, “the currently-accessed memory device other than the segment currently being replaced by the first non-volatile memory device” lacks antecedent basis.

As per claim 15, lines 12-13, “other than segments replaced by the first type non-volatile memory device” is not clear in context. It is noted that “segments replaced by the first type non-volatile memory device” have not been set forth previously in claims 1 and/or 6, nor has any relationship between the “replacement area” and the “first type non-volatile memory device” been set forth.

As per claim 16, it is not clear what Applicant is trying to set forth in this claim. In lines 1 and 4, it is not clear what is meant by “the in the replacement memory area”. In line 4, “the replacement memory area that is currently being replaced by the first type non-volatile memory device” lacks antecedent basis since a first type non-volatile memory device has not been set forth specifically as replacing the replacement memory area.

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As per claim 17, lines 3-4, “the currently-accessed memory device” lacks antecedent basis. In line 4, “the one second memory replacement segment” lacks antecedent basis since no particular second memory replacement segment has been set forth previously. In lines 5-6, “the currently-accessed memory device other than the segment currently being replaced by the first non-volatile memory device” lacks antecedent basis.

All dependent claims are rejected as having the same deficiencies as the claims they depend from.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1 and 10 are rejected under 35 U.S.C. 102(b) as being anticipated by Bowers (6,223,147).

As per claim 1, Bowers teaches a system including a multiple use chip socket (“multi-memory architecture”) which supports both a FLASH device (“first type non-volatile memory”) and an EPROM device (“second type non-volatile memory”). See column 2, lines 38-40. Each device has a data storage capacity (it is noted that the devices may have the same capacity in accordance with the claim language, although from the comparison shown in figure 4 it appear that the FLASH memory has). The pin configuration of the socket is the same as that of the FLASH device (“total pin number...is equal to the number of pins of the first type non-volatile

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memory device”). See figures 3-4 and column 5, lines 1-2. The Examiner notes that claim 1 does not require that the first and second memory devices be in the multi-memory architecture at the same time. The FLASH device has 32 pins (column 5, line 1) and the EPROM has 28 pins (column 3, line 30) (“the first number of pins is greater than the second number of pins”).

As per claim 10, Bowers teaches a system including a multiple use chip socket (“multi-memory architecture”) which supports both a FLASH device (“first type non-volatile memory”) and an EPROM device (“second type non-volatile memory”). See column 2, lines 38-40. Each device has a data storage capacity (it is noted that the devices may have the same capacity in accordance with the claim language, although from the comparison shown in figure 4 it appears that the FLASH memory has). The pin configuration of the socket is the same as that of the FLASH device. See figures 3-4 and column 5, lines 1-2. The Examiner notes that claim 10 does not require that the first and second memory devices be in the multi-memory architecture at the same time.

Remarks

6. No prior art has been applied to claims 6-8 and 15-17 given the ambiguities in the claims as set forth in the rejection of the claims under 35 U.S.C. 112, second paragraph, above.

Allowable Subject Matter

7. Claims 2-5 and 11-14 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base

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claim and any intervening claims, as well as overcoming any objections to the claims set forth above.

Conclusion

8. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, D.C. 20231

All "OFFICIAL" patent application related correspondence transmitted by FAX must be directed to the central FAX number at **(703) 872-9306**:

"INFORMAL" or "DRAFT" FAX communications may be sent to the Examiner at **(571) 273-4204**, only after approval by the Examiner.

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA., Fourth Floor (receptionist).

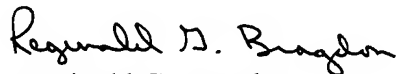
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9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Reginald G. Bragdon whose telephone number is (571) 272-4204. The examiner can normally be reached on Monday-Thursday from 7:00 AM to 4:30 PM and every other Friday from 7:00 AM to 3:30 PM.

The examiner's supervisor, Mano Padmanabhan, can be reached at (571) 272-4210.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 305-3900.

RGB
December 21, 2005


Reginald G. Bragdon
Primary Patent Examiner
Art Unit 2188